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FIG. 1A

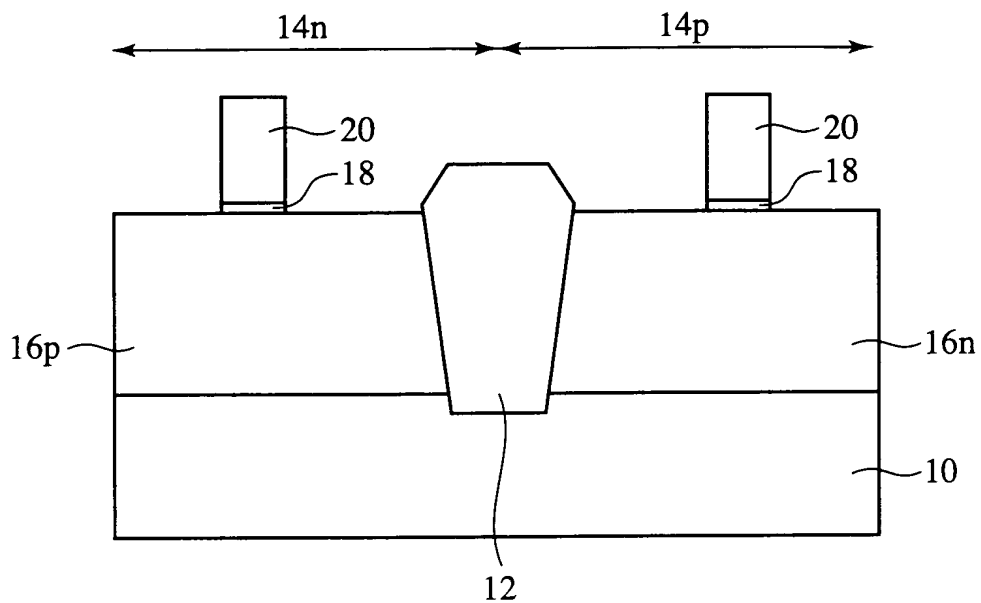
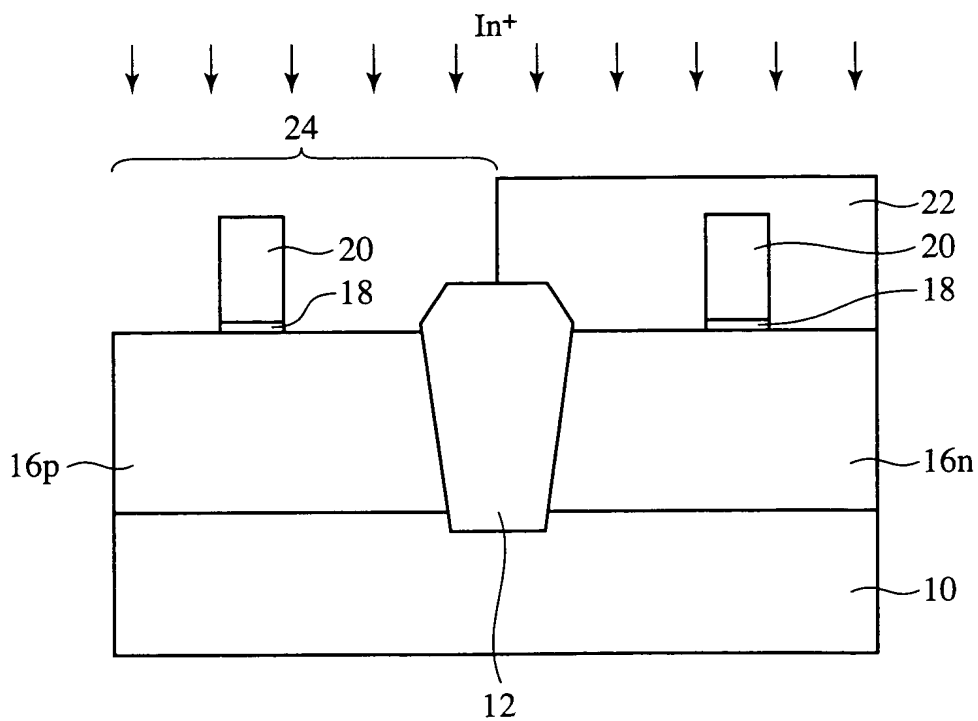


FIG. 1B



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FIG. 2A

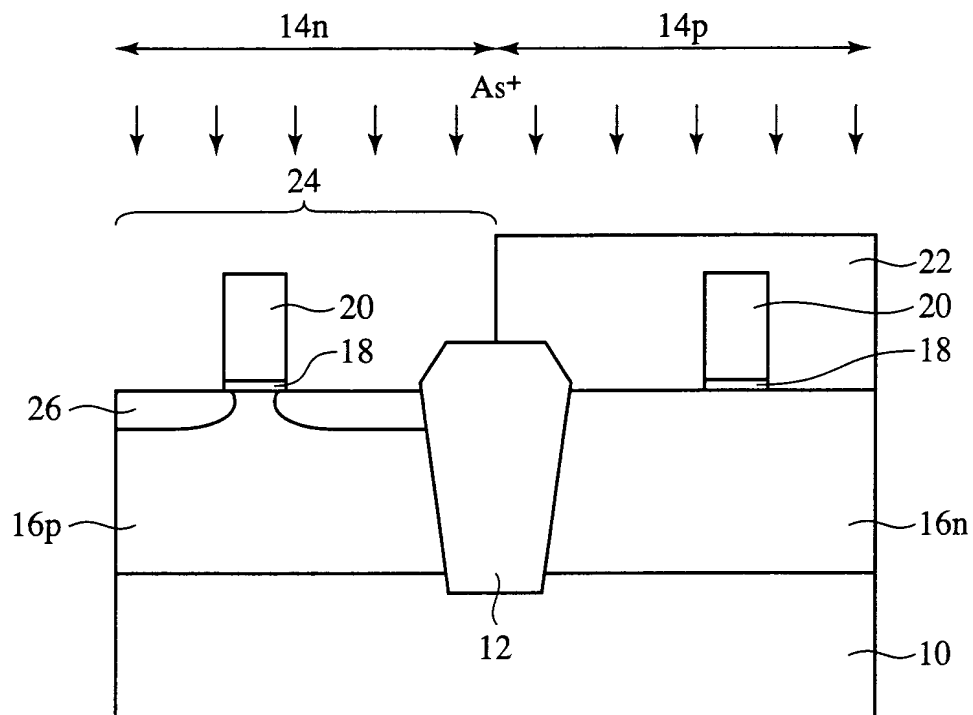
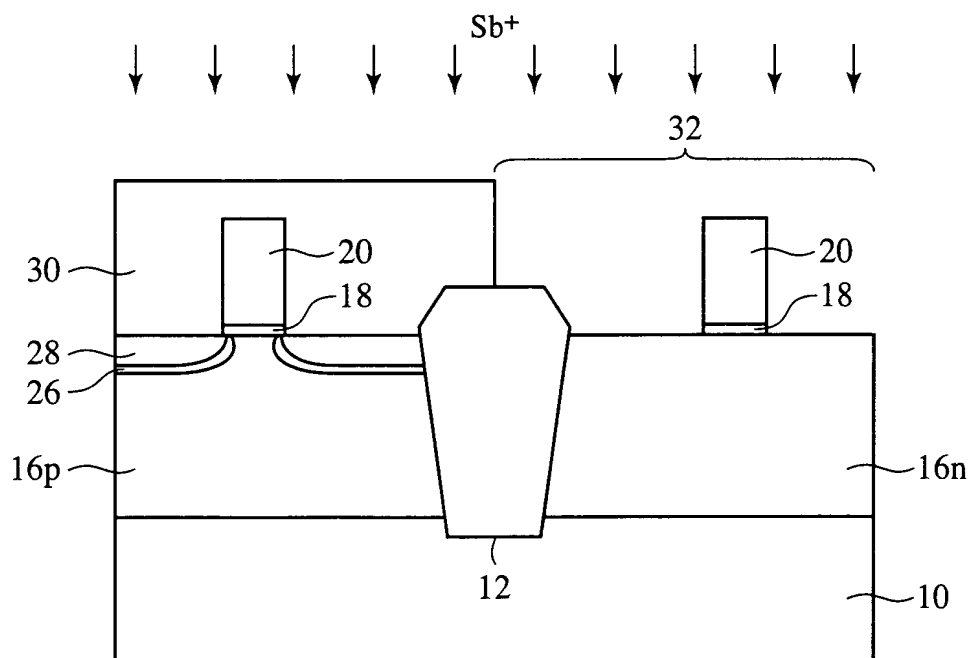


FIG. 2B



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FIG. 3A

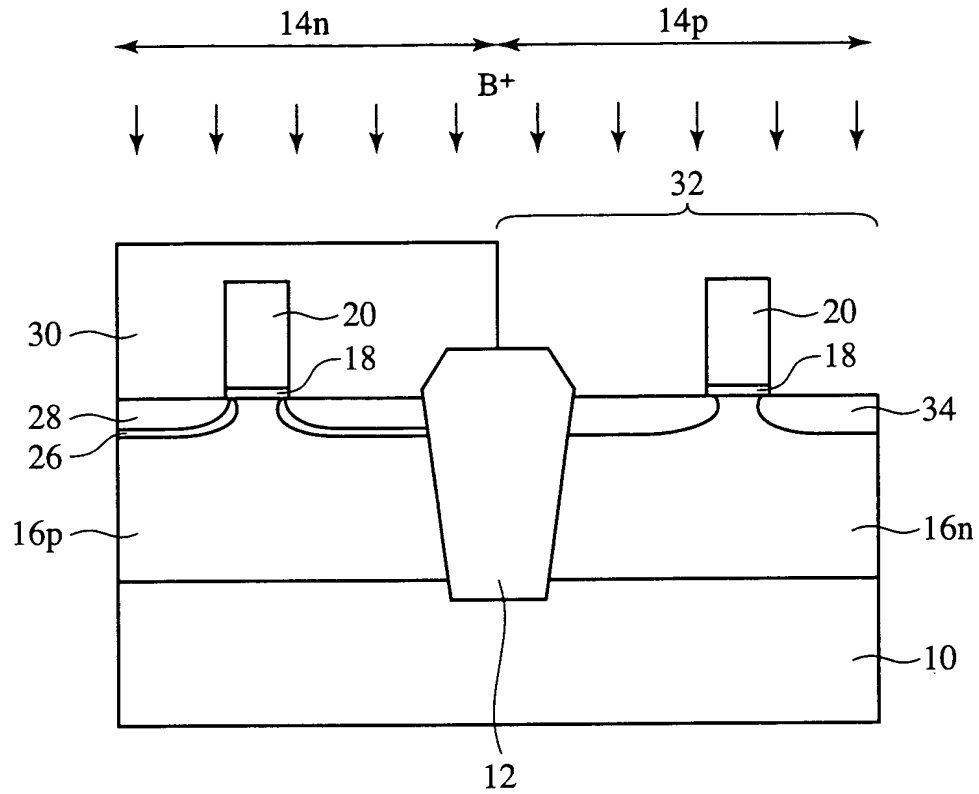
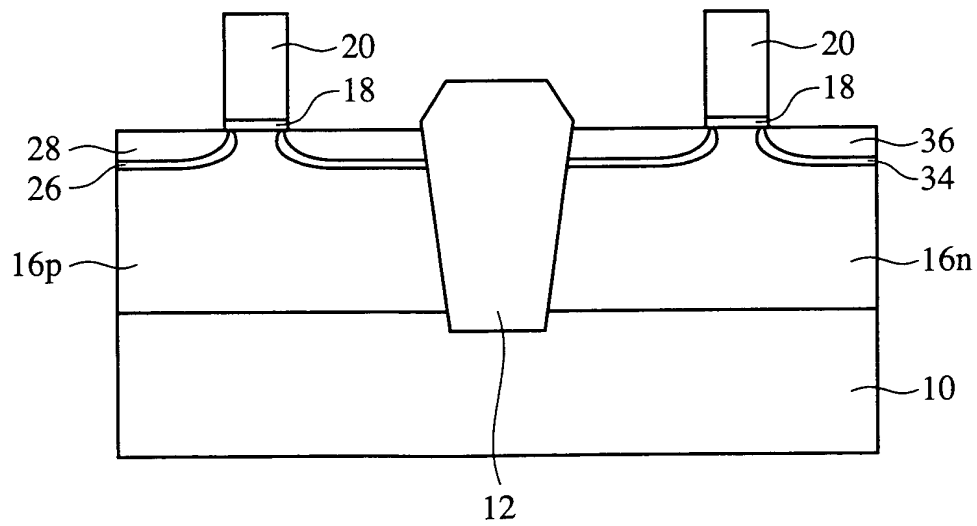


FIG. 3B



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FIG. 4A

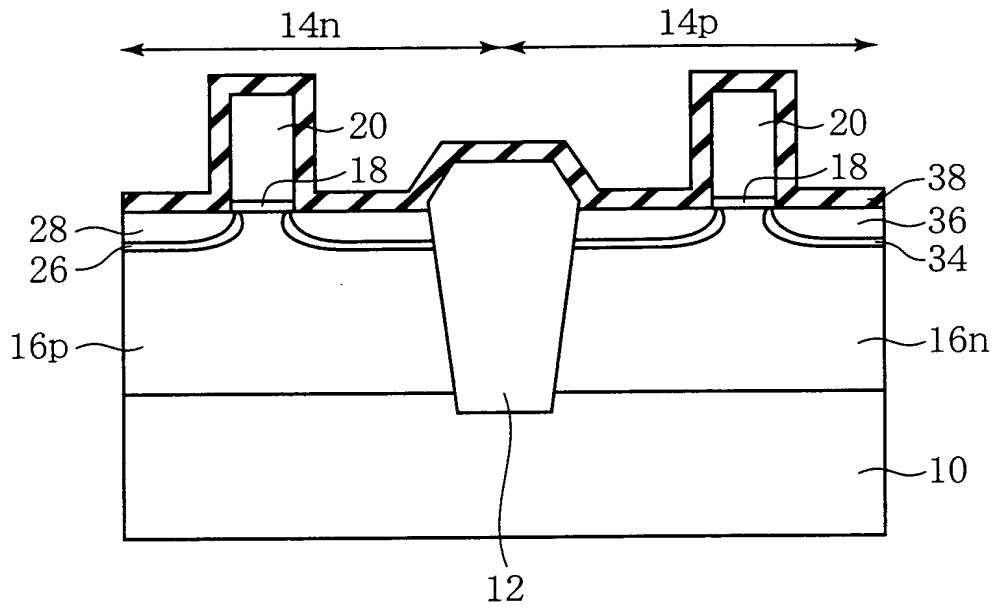
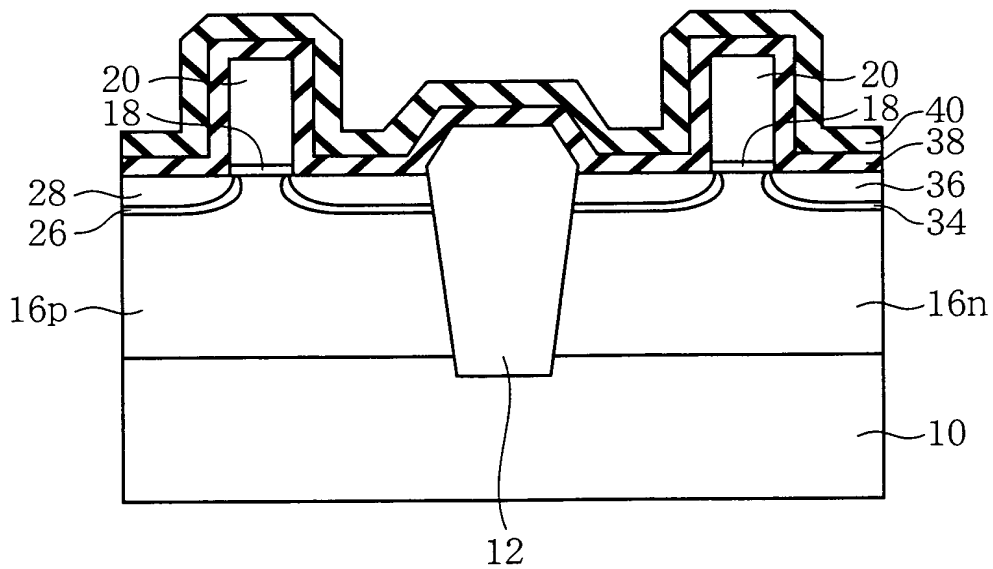


FIG. 4B



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FIG. 5A

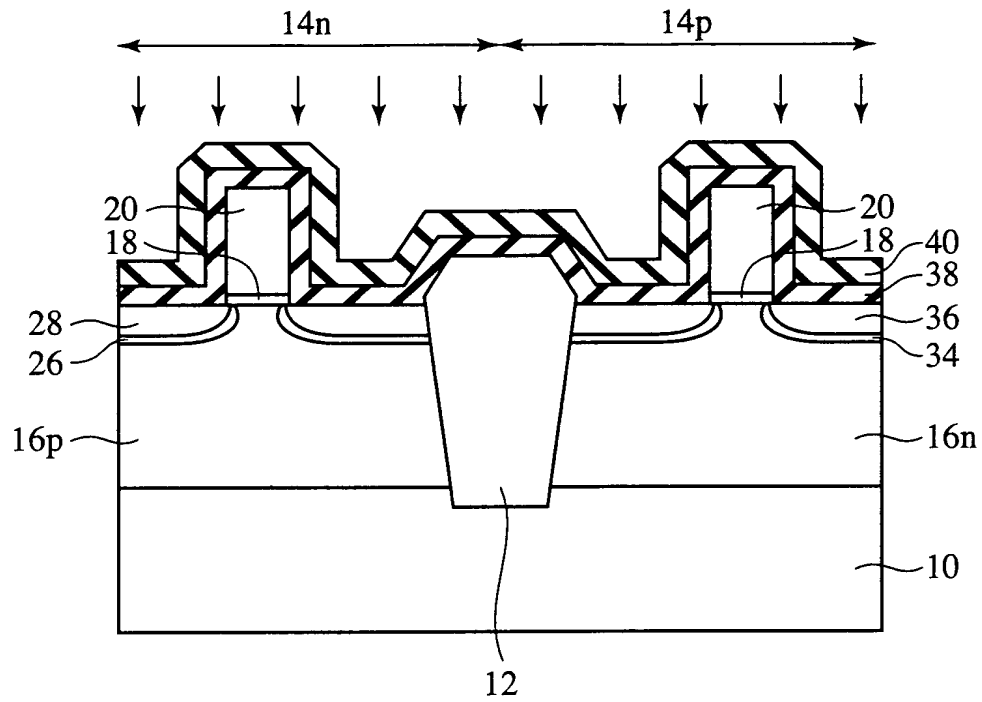
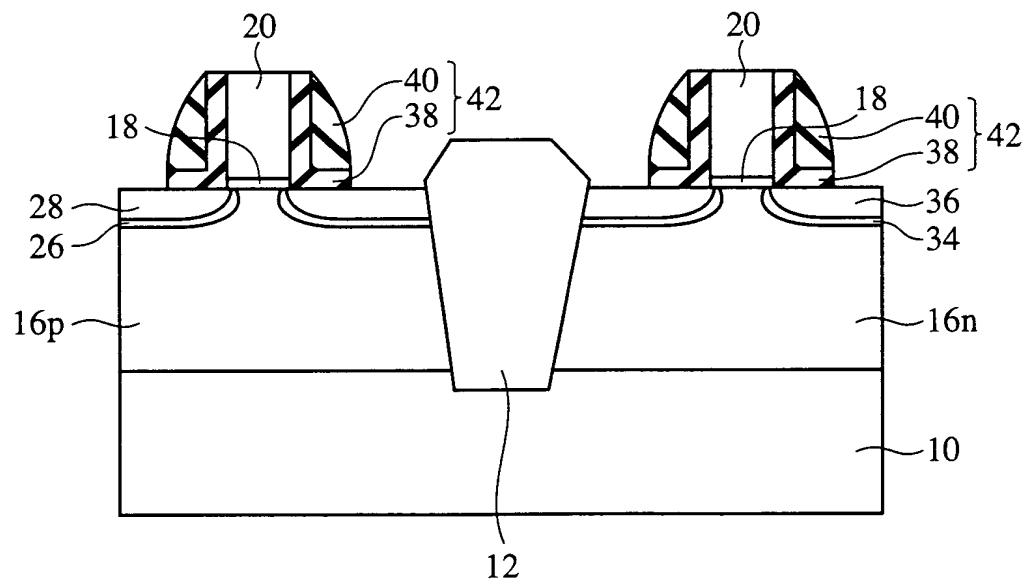


FIG. 5B



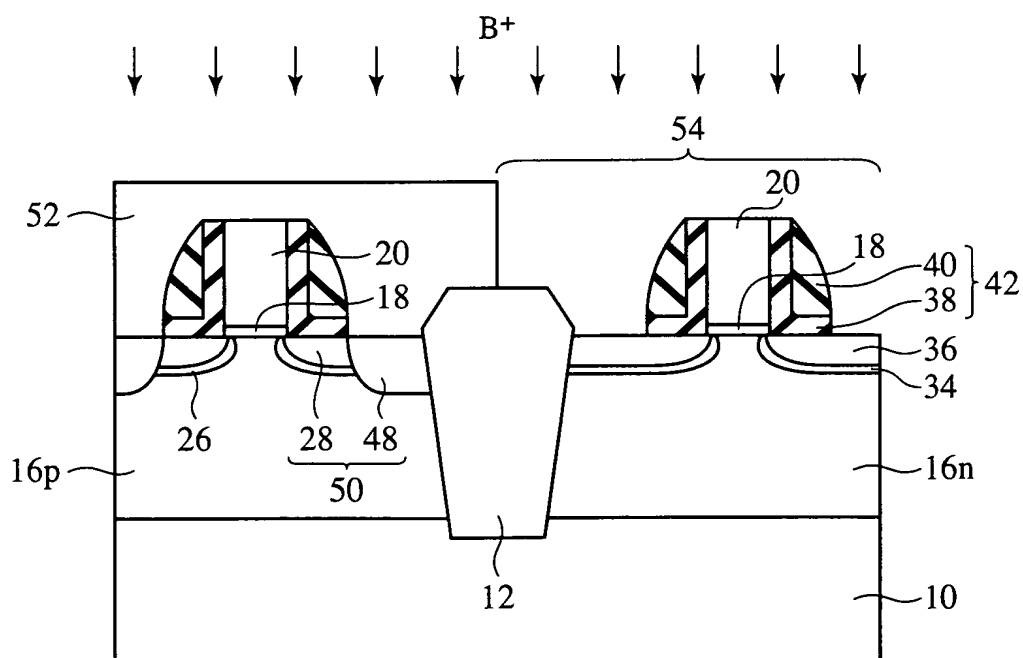


FIG. 7A

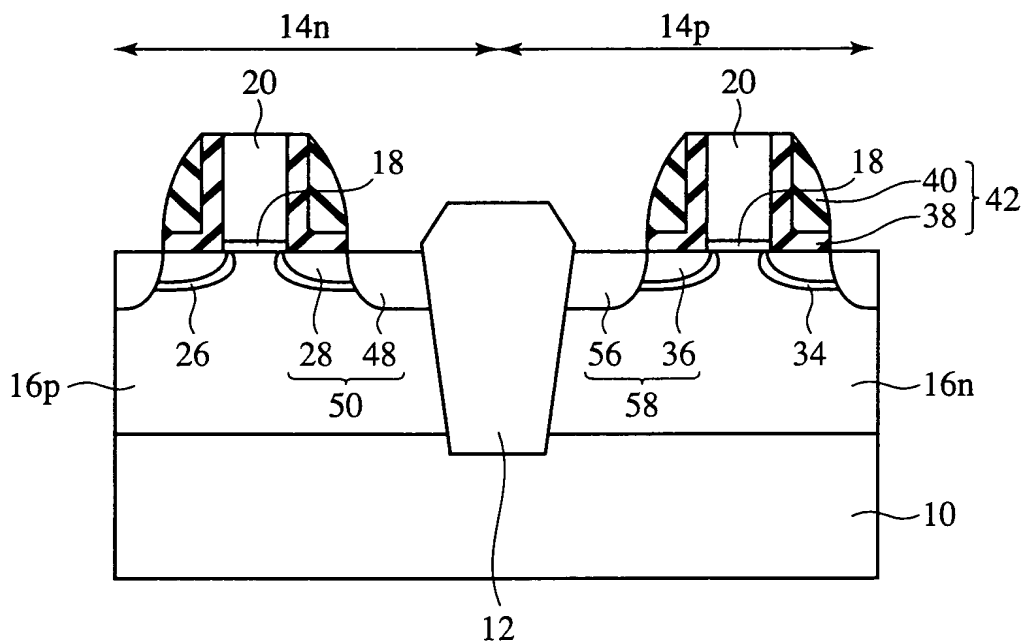
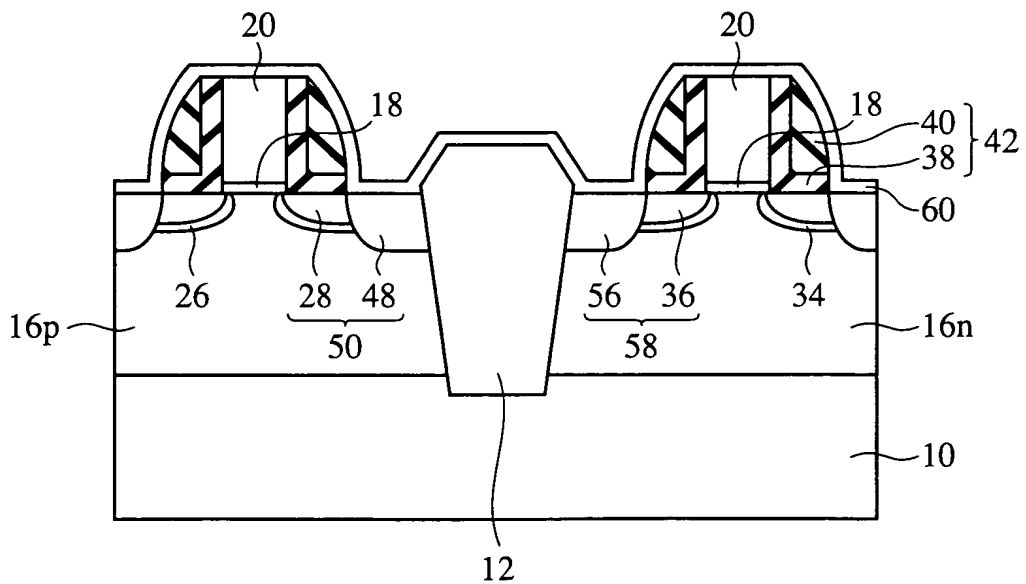
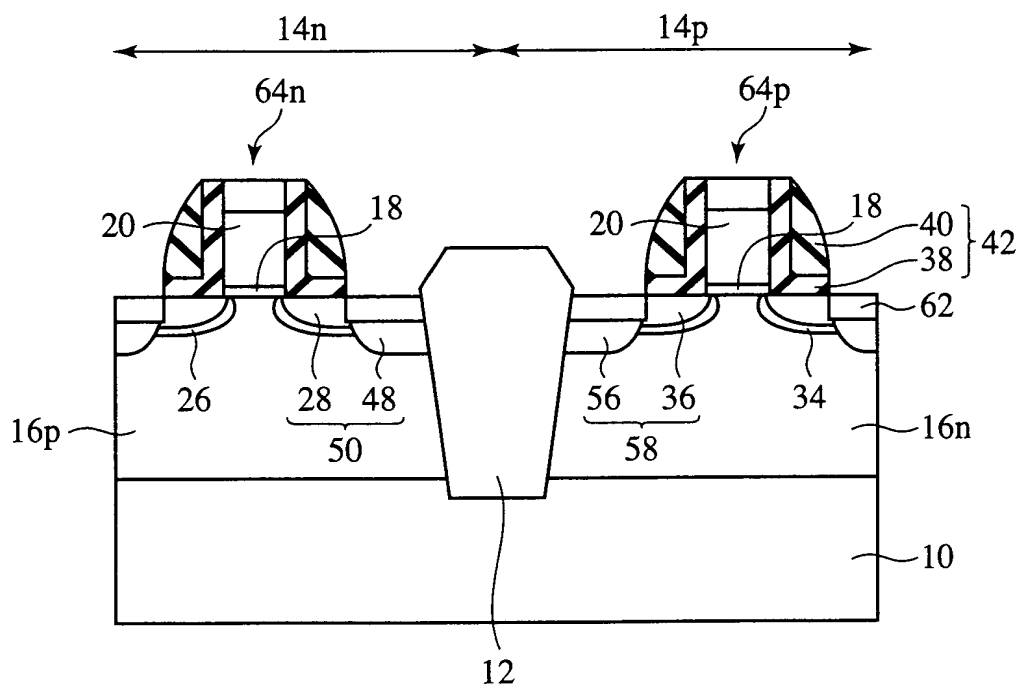


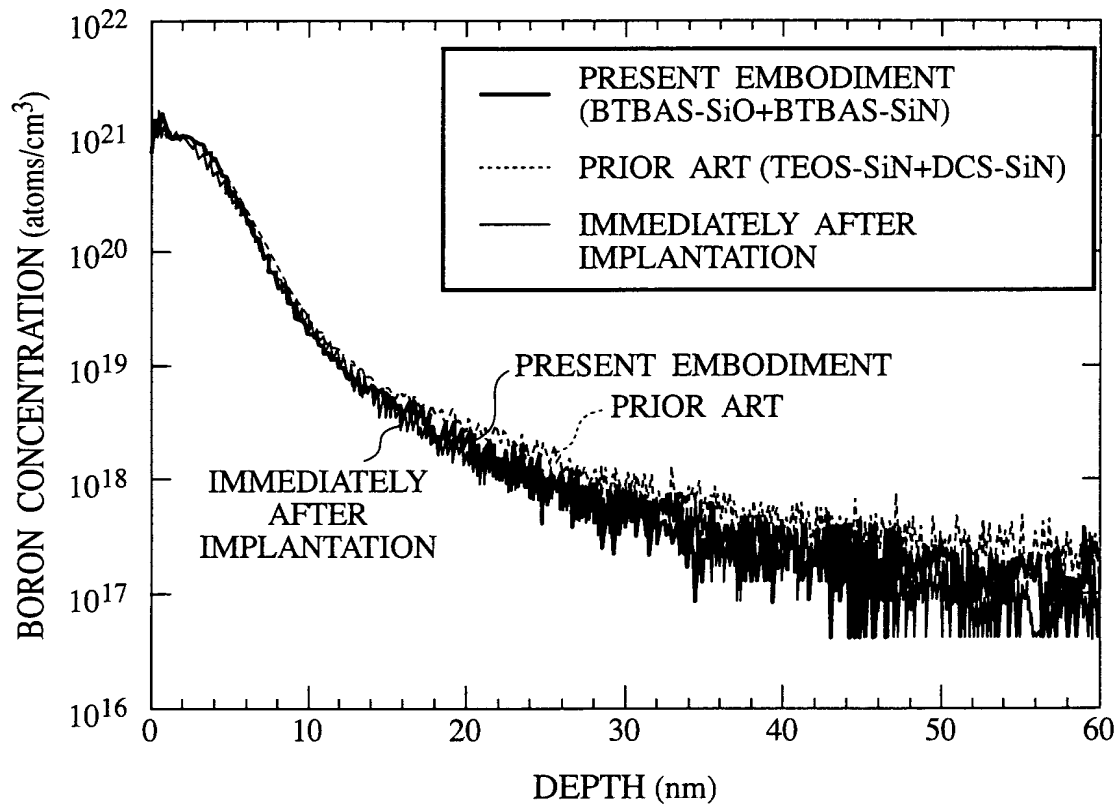
FIG. 7B





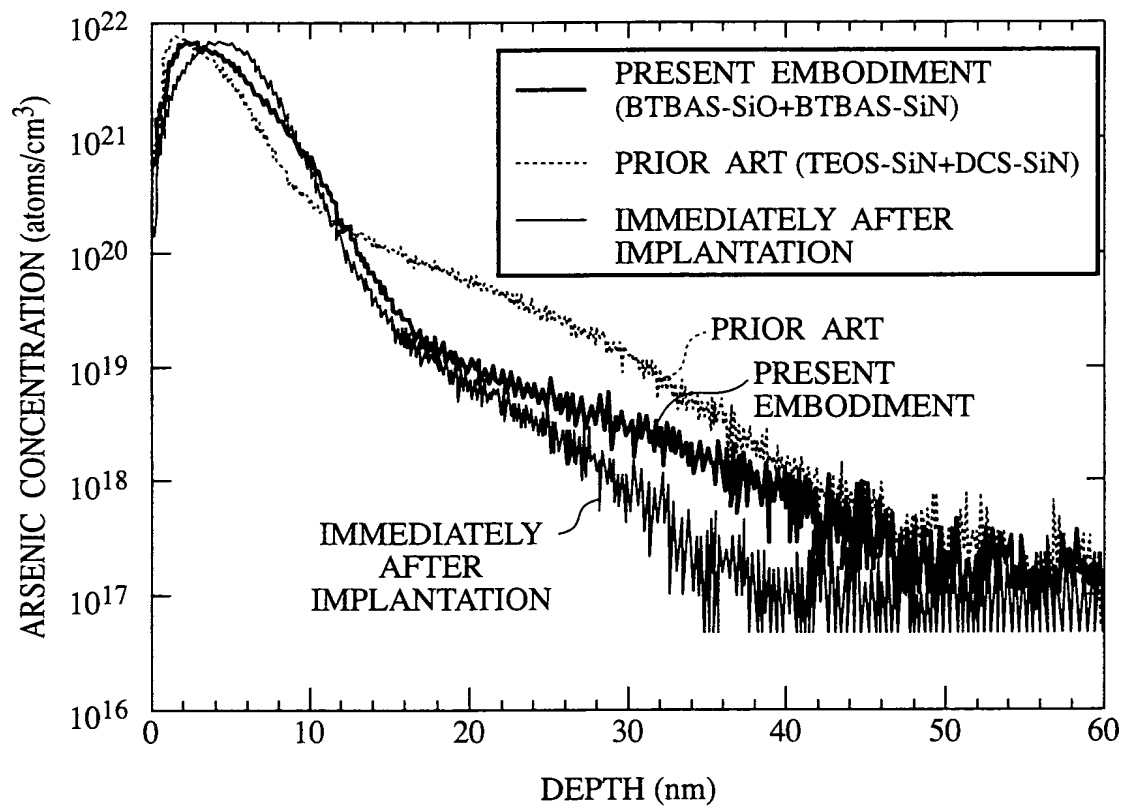
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FIG. 9



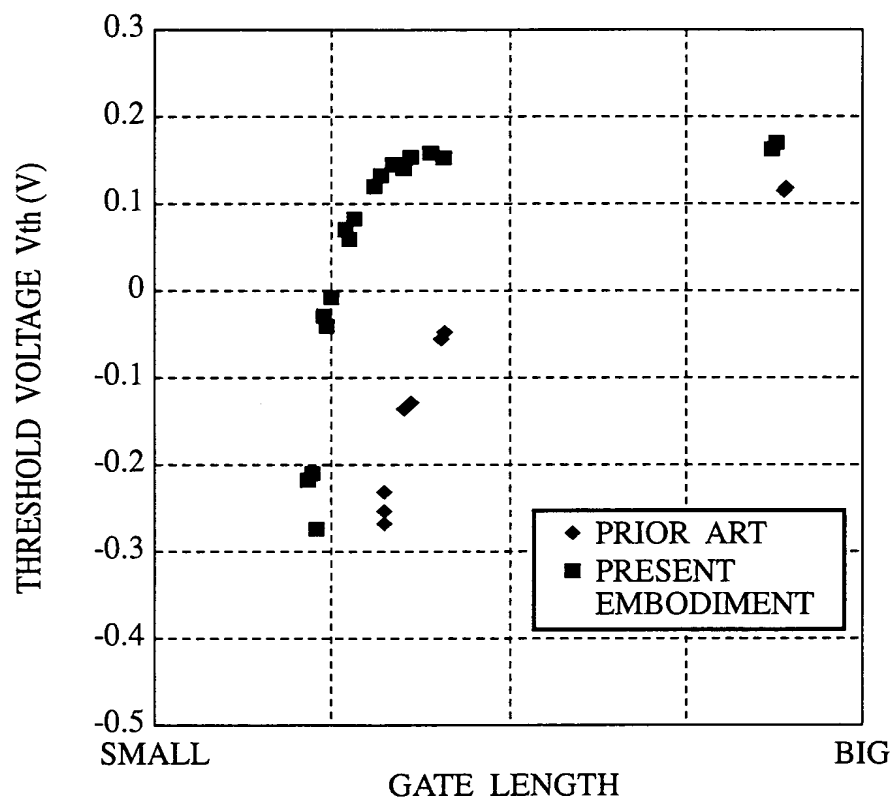
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FIG. 10



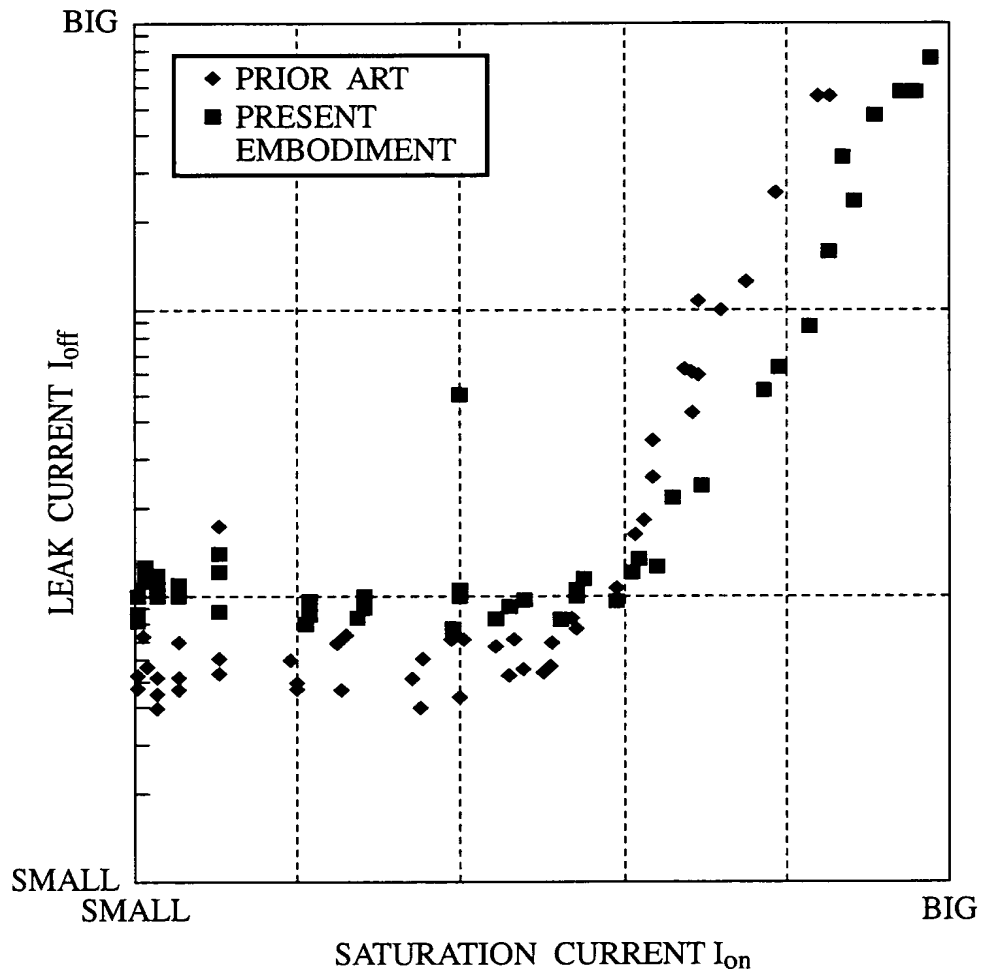
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FIG. 11



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FIG. 12



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FIG. 13A

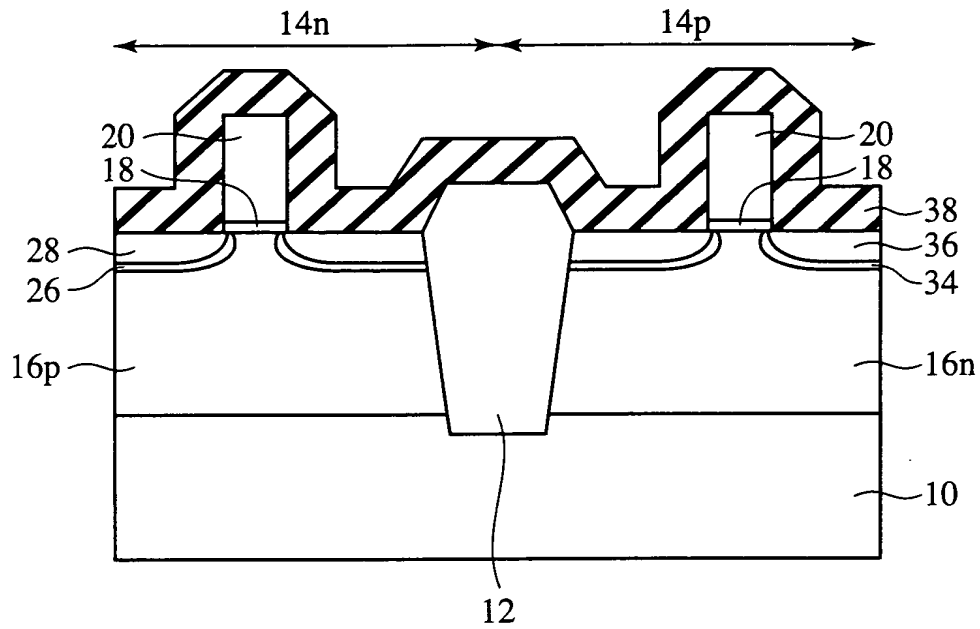
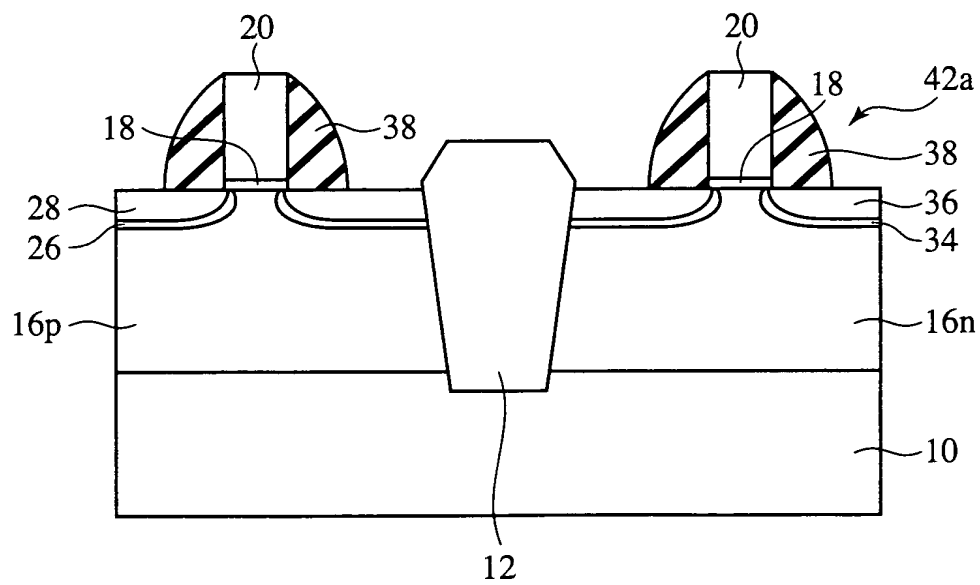


FIG. 13B



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FIG. 14A

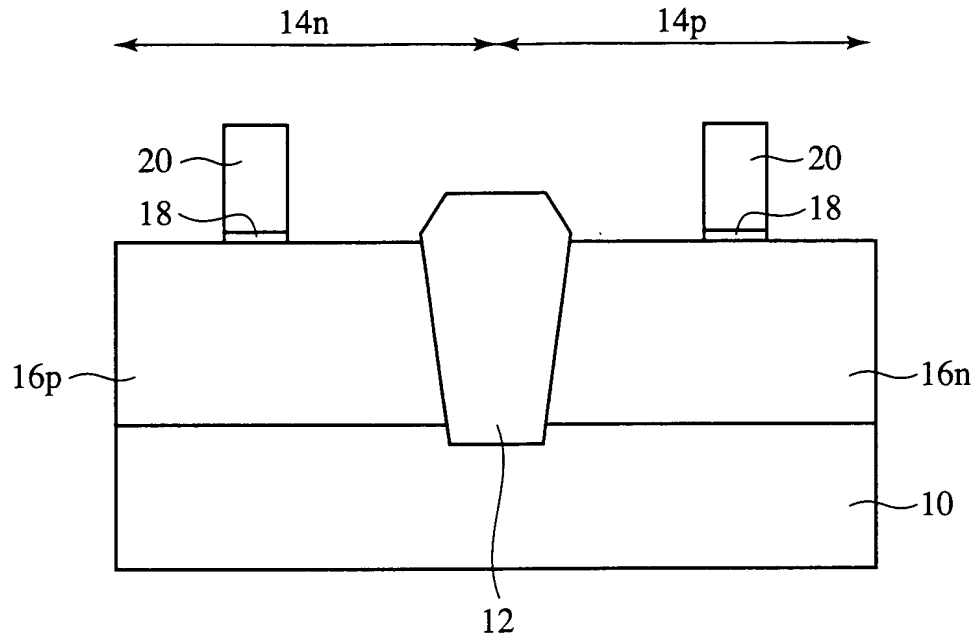
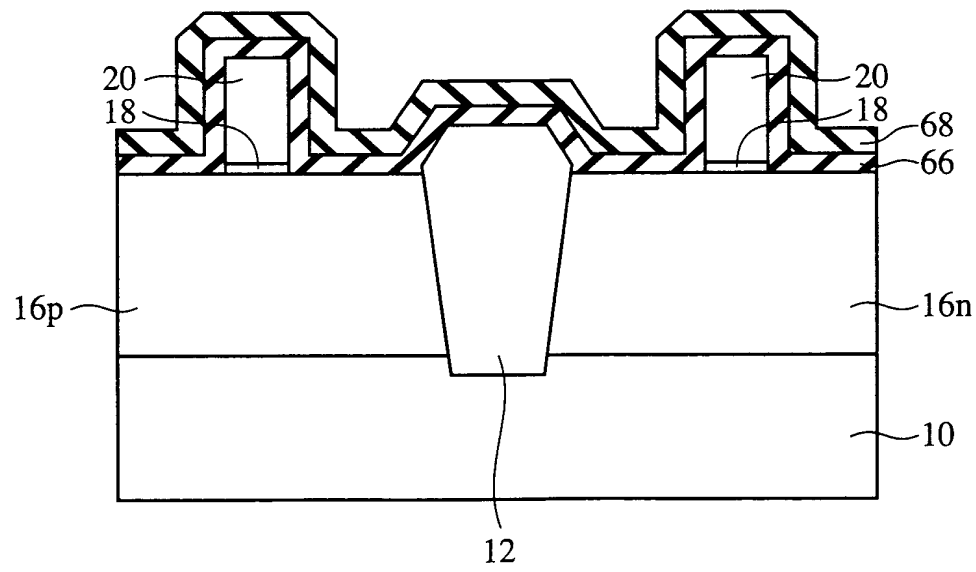


FIG. 14B



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FIG. 15A

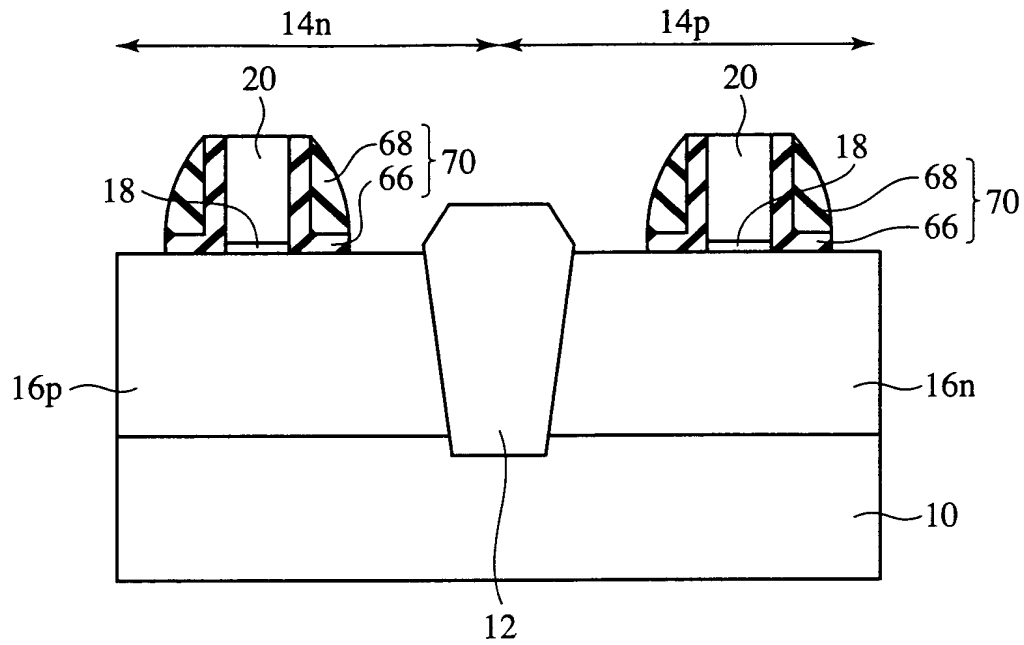
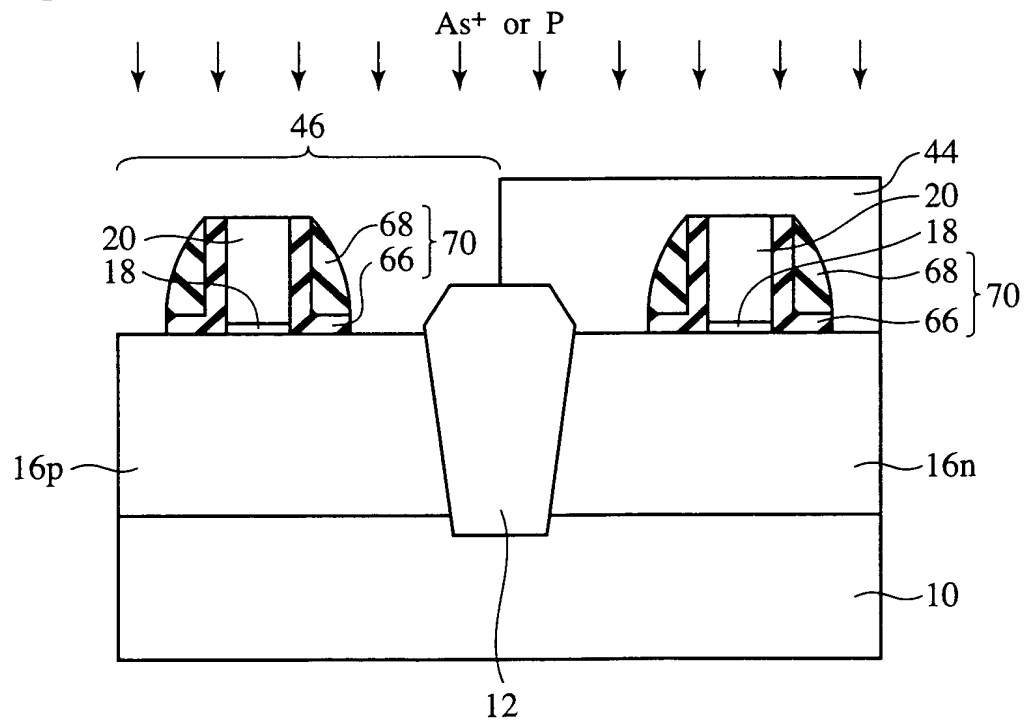


FIG. 15B



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FIG. 16A

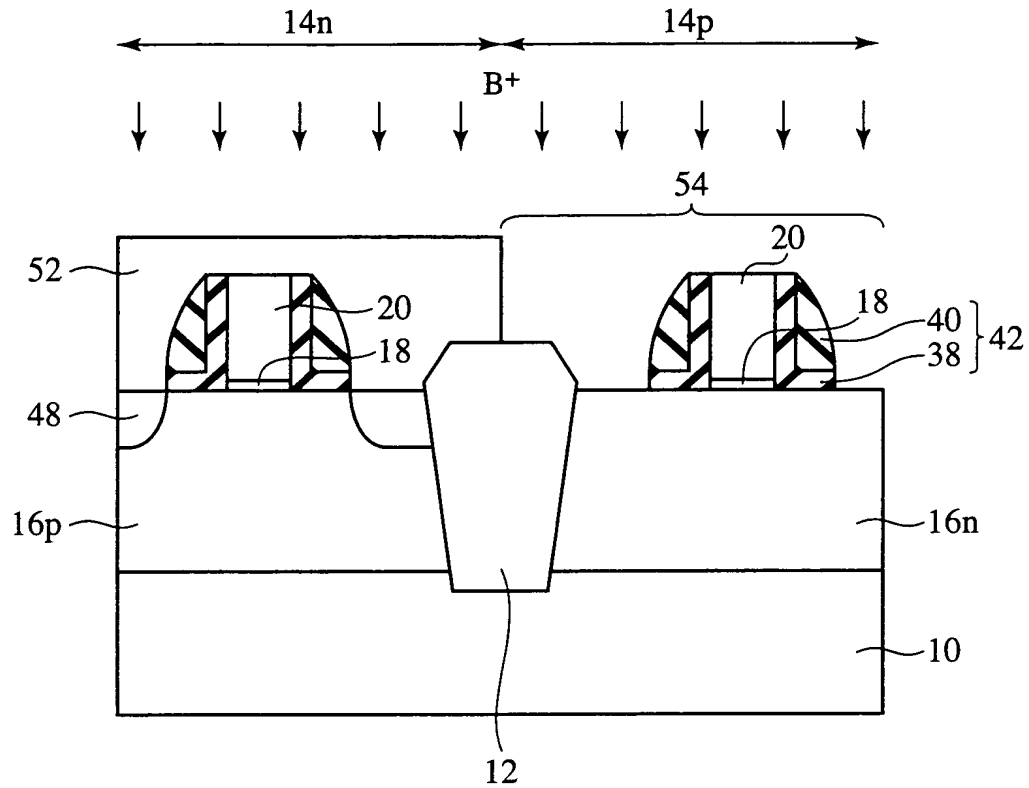
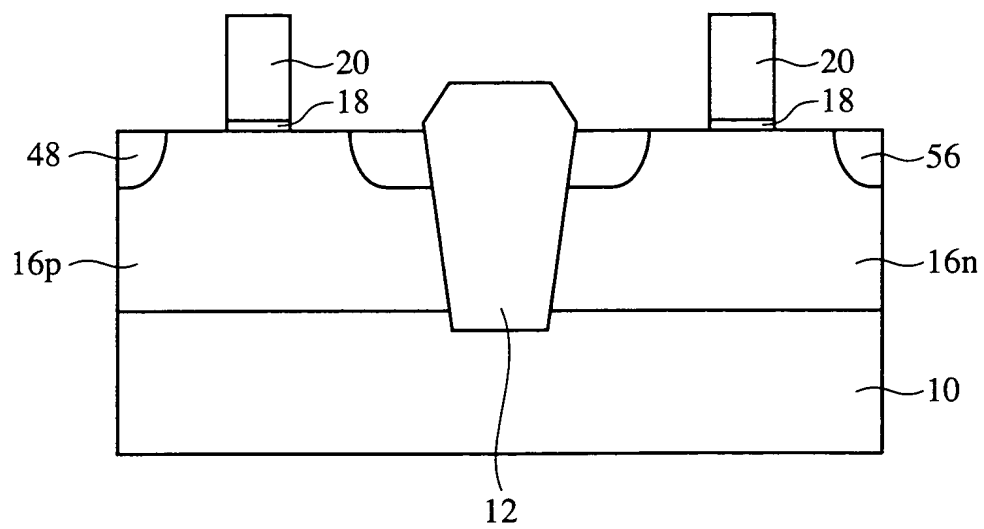


FIG. 16B



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FIG. 17A

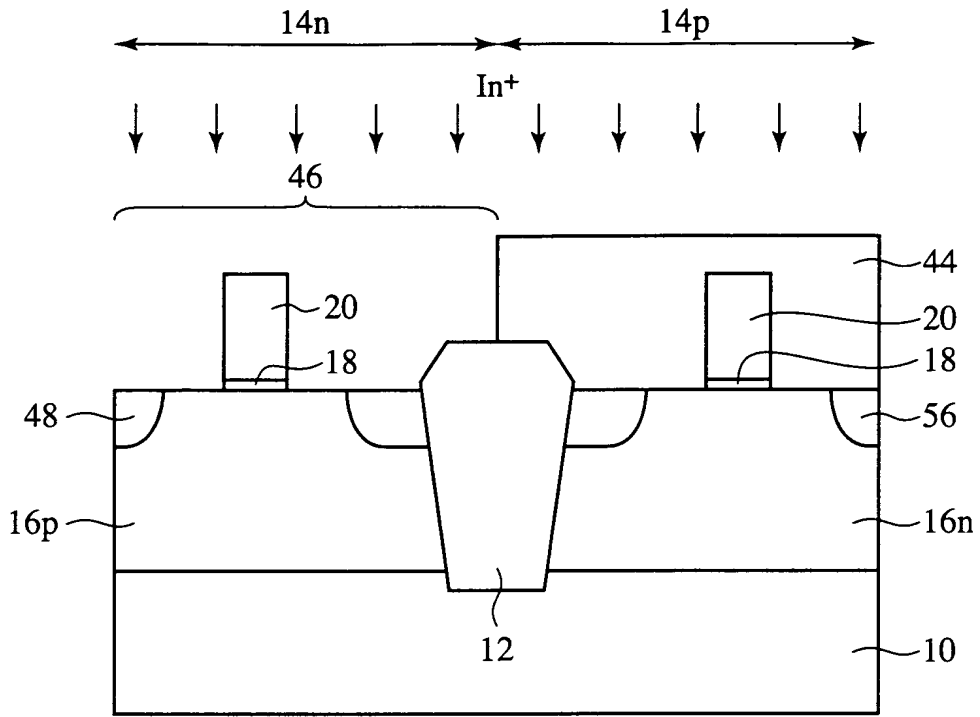
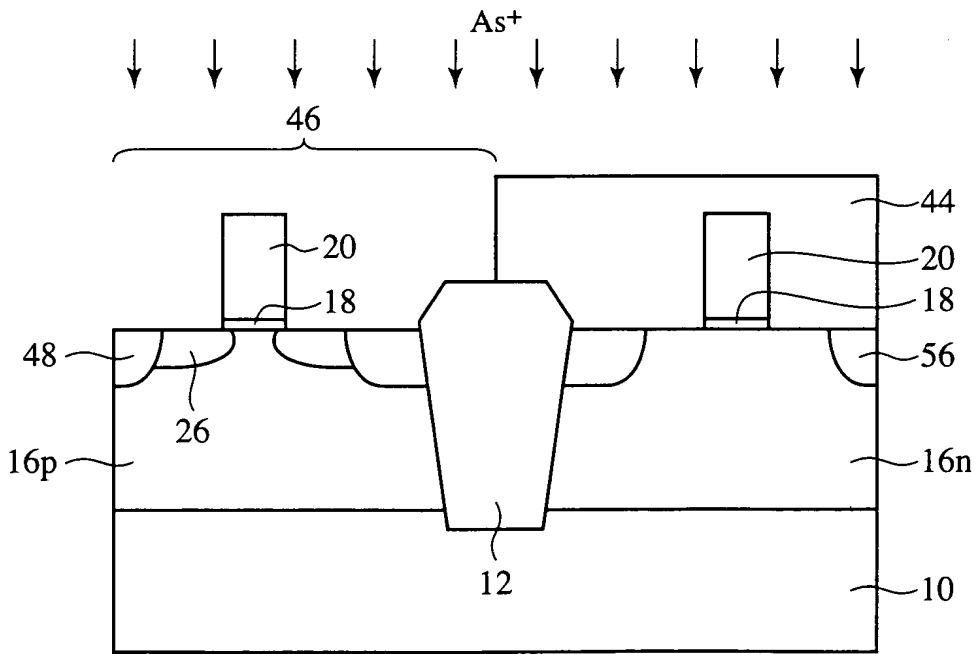


FIG. 17B



A cross-sectional view of a semiconductor device. A central gate structure 12 is formed on a substrate 10. The gate structure 12 is surrounded by a gate dielectric layer 16n. On either side of the gate structure 12, there are side regions 16p. In each side region 16p, there is a rectangular region 20 containing a smaller rectangular region 18. A layer 52 is formed on top of the side regions 16p. A layer 54 is formed on top of the gate structure 12. A layer 56 is formed on top of the side regions 16p. A layer 50 is formed on top of the side regions 16p. A layer 26 is formed on top of the side regions 16p. A layer 28 is formed on top of the side regions 16p. A layer 48 is formed on top of the side regions 16p. A layer 14n is formed on top of the side regions 16p. A layer 14p is formed on top of the side regions 16p. A layer Sb<sup>+</sup> is formed on top of the side regions 16p. Arrows indicate the direction of the layers.

[illegible]

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FIG. 19A

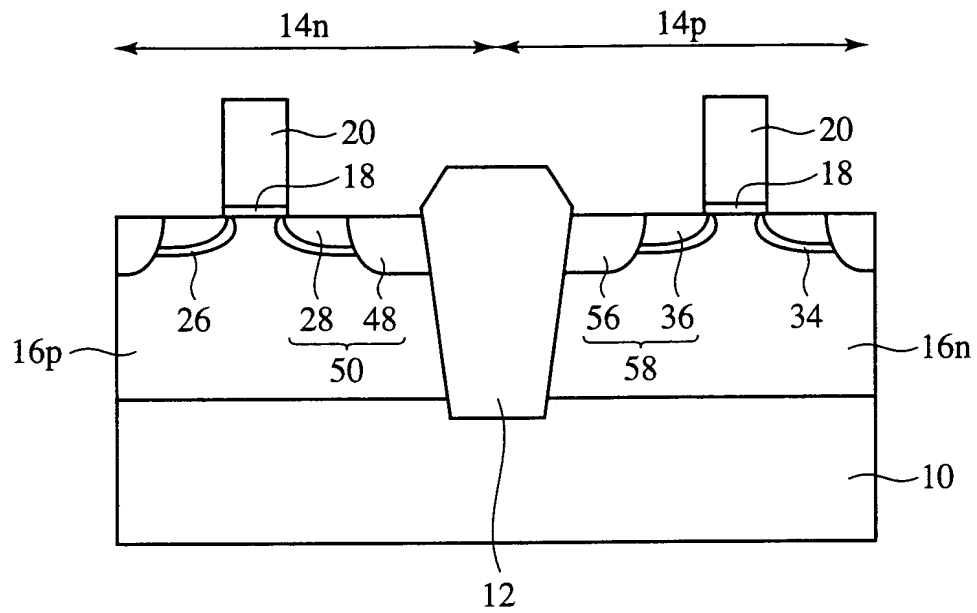
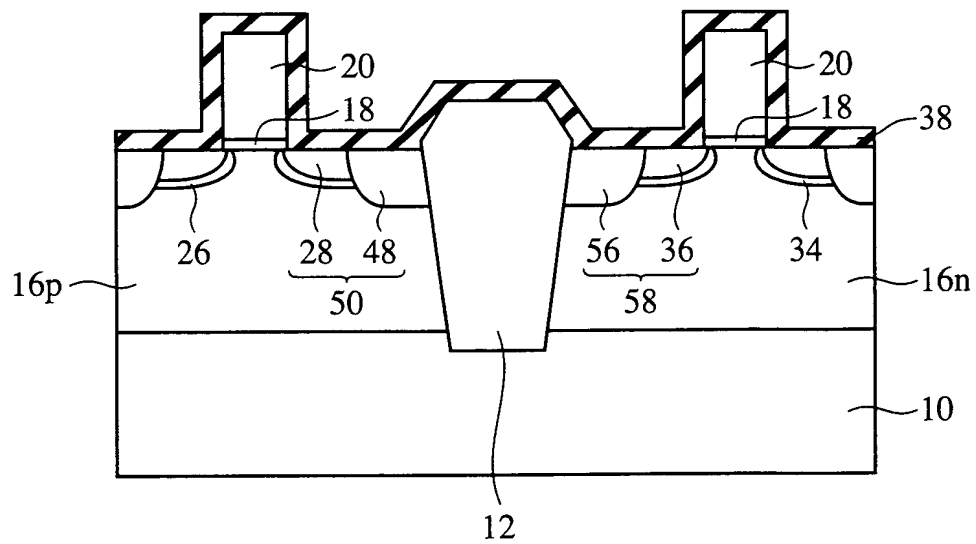


FIG. 19B



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FIG. 20A

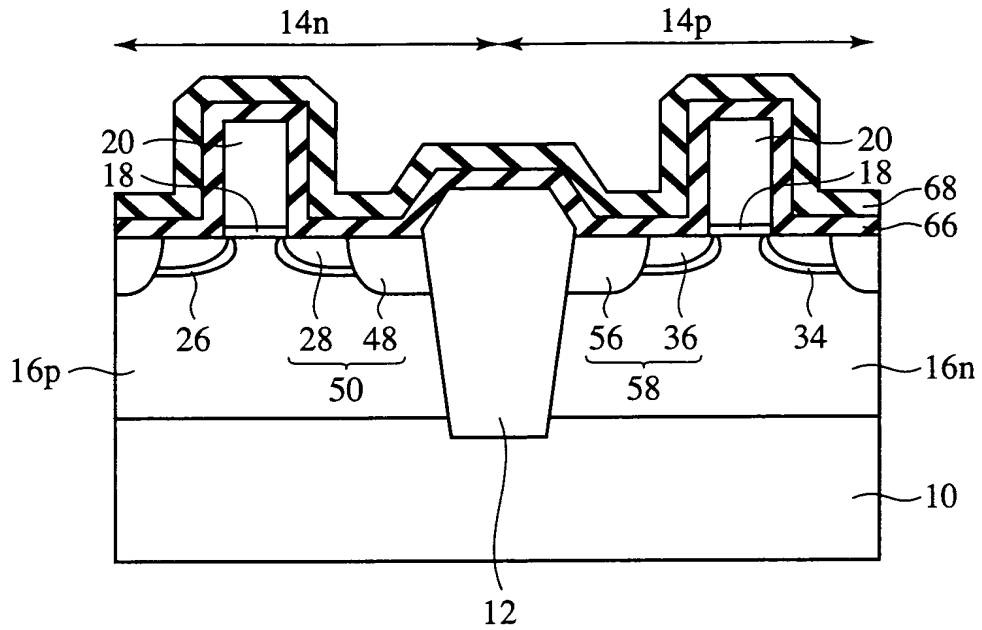
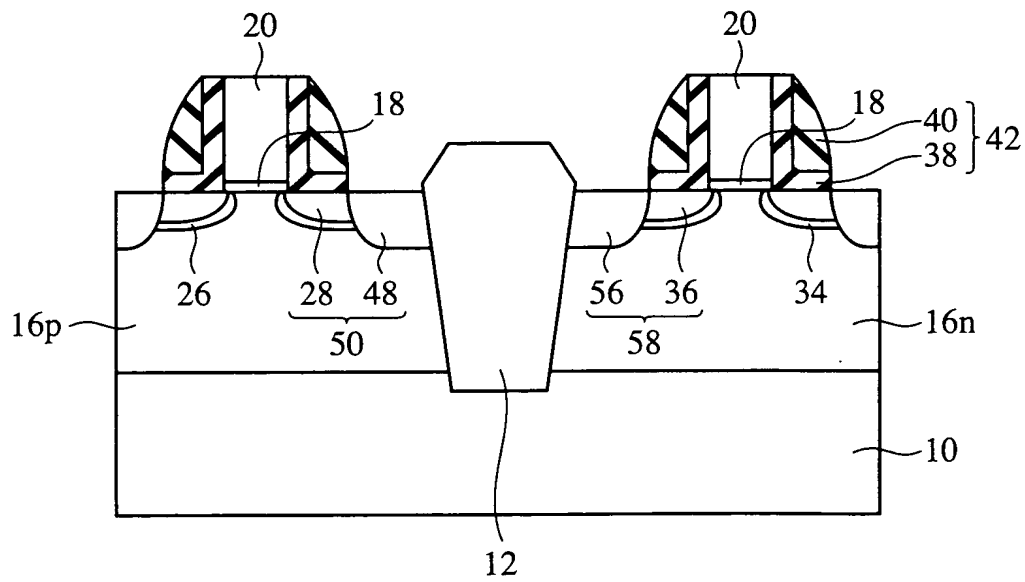


FIG. 20B



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FIG. 21A

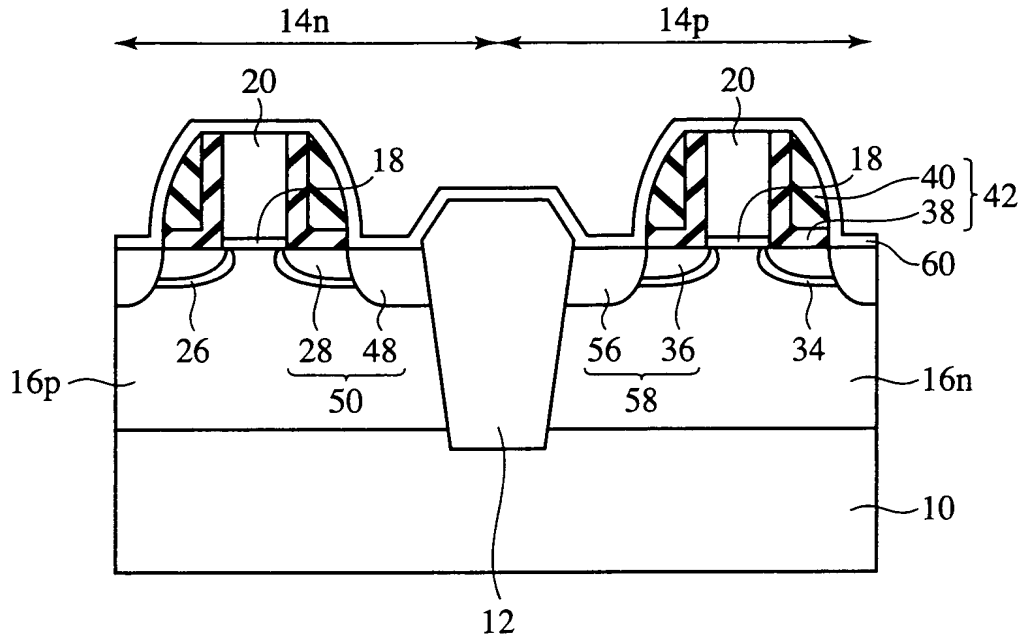
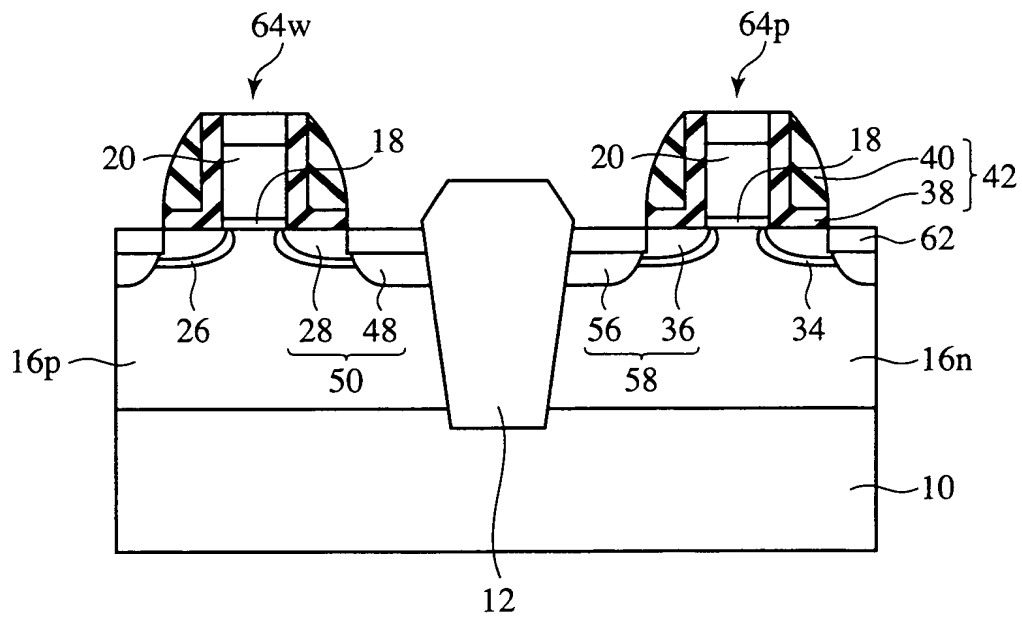
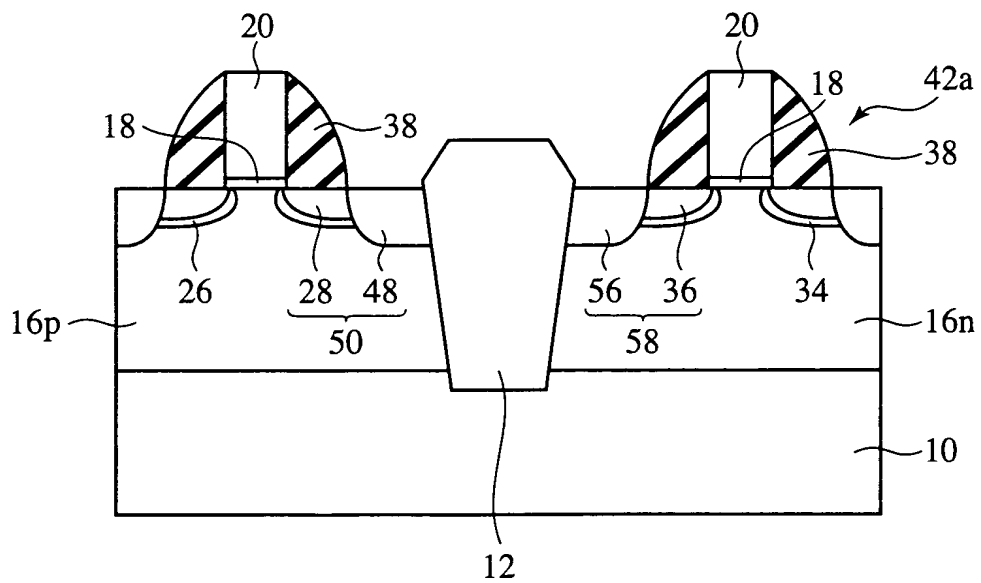


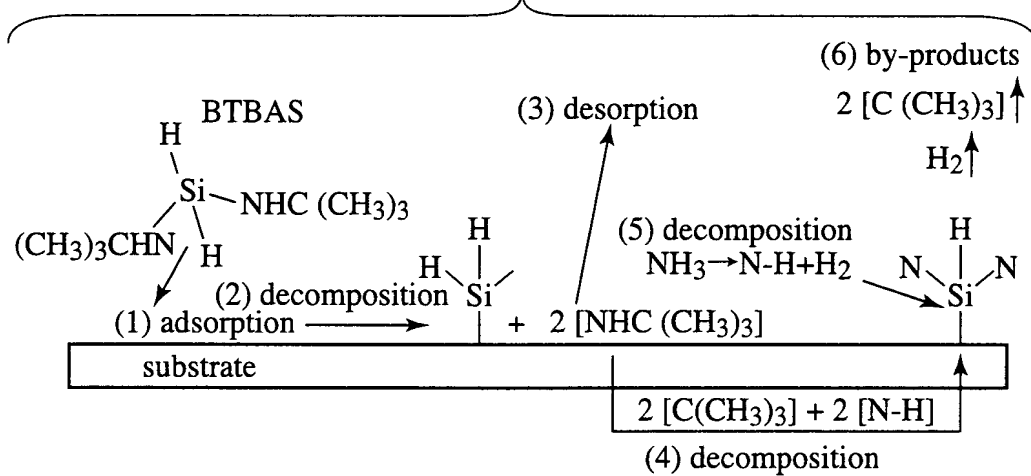
FIG. 21B





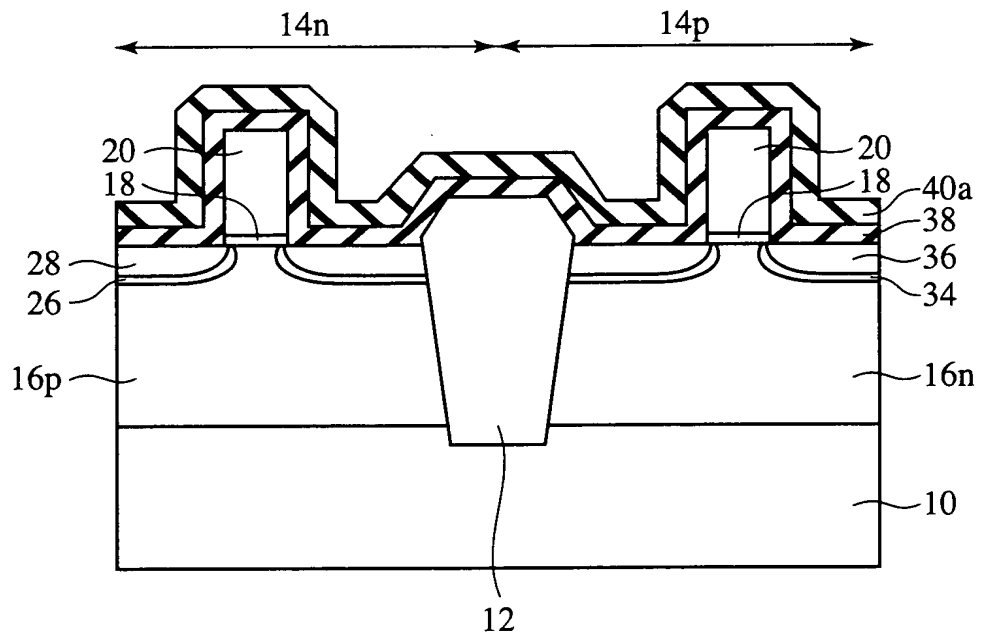
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FIG. 23



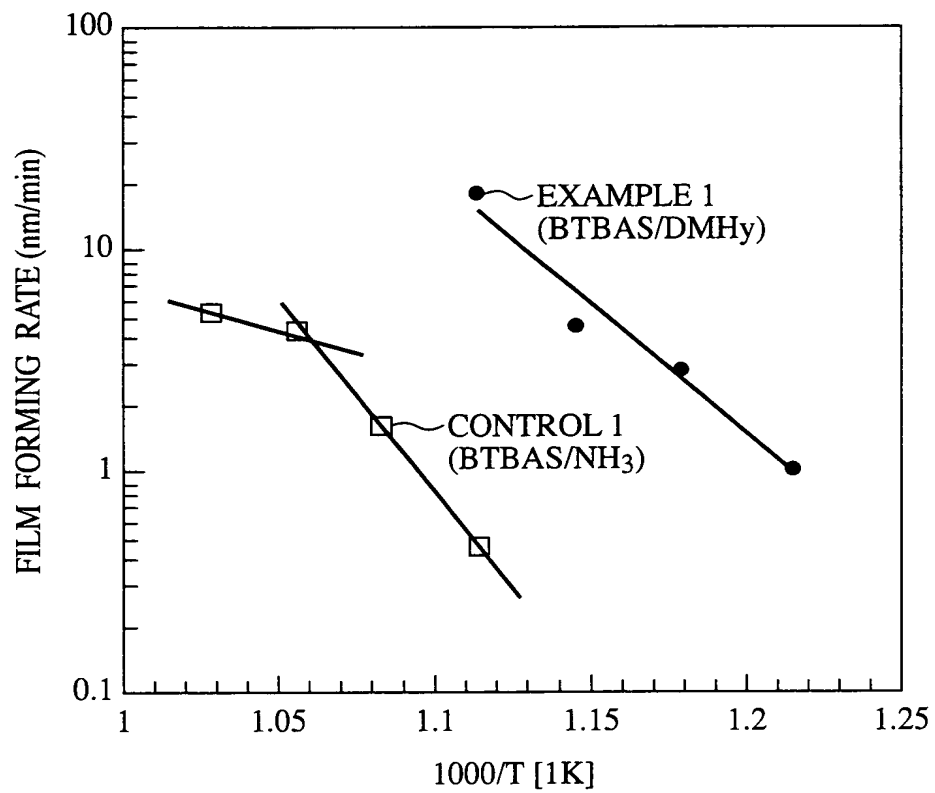
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FIG. 24



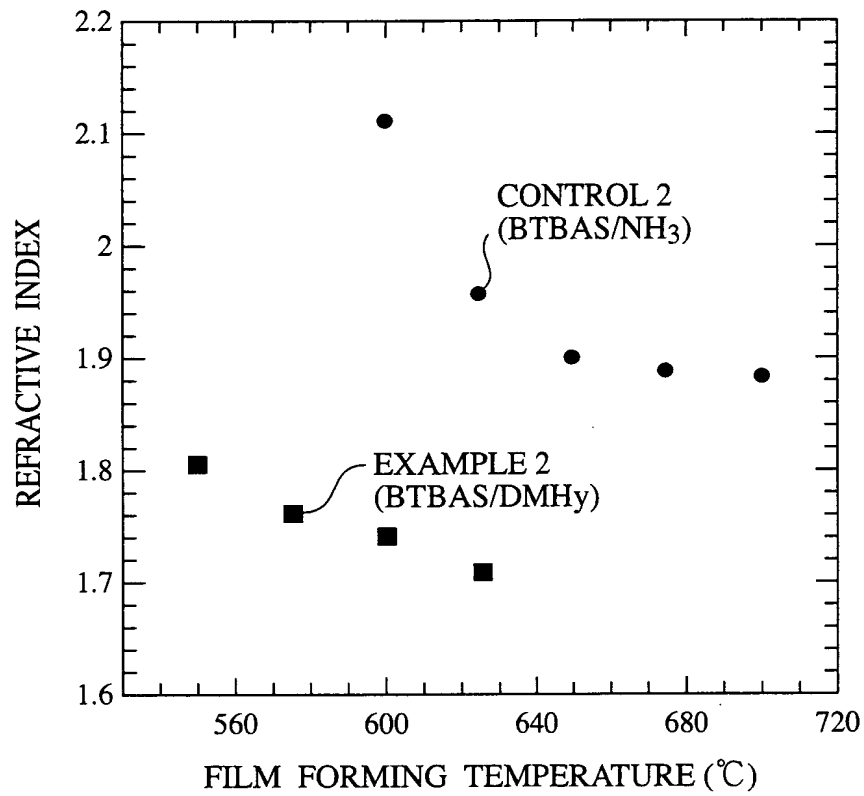
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FIG. 25



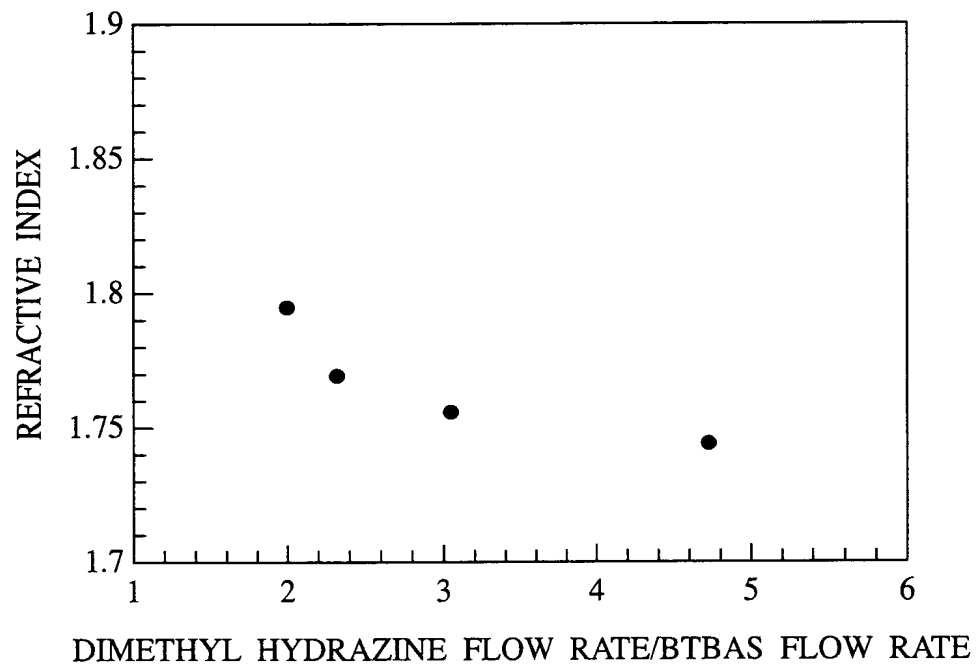
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FIG. 26



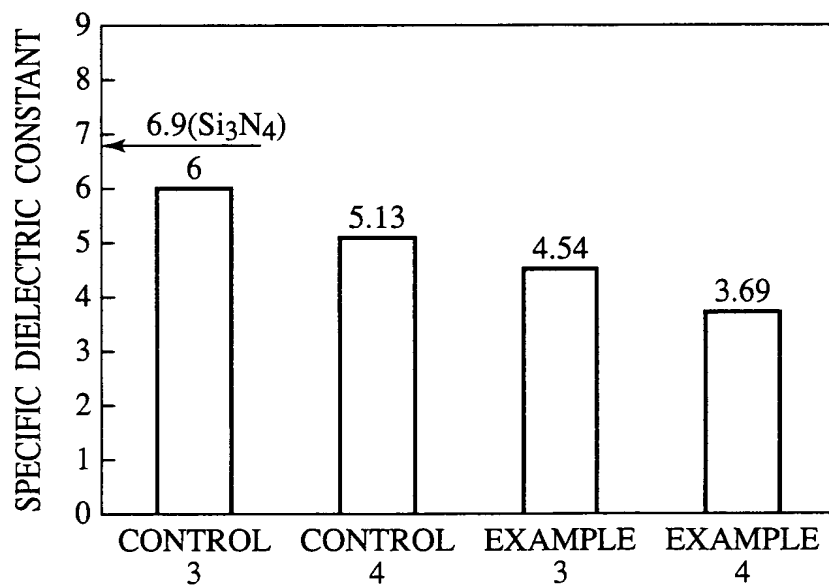
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FIG. 27



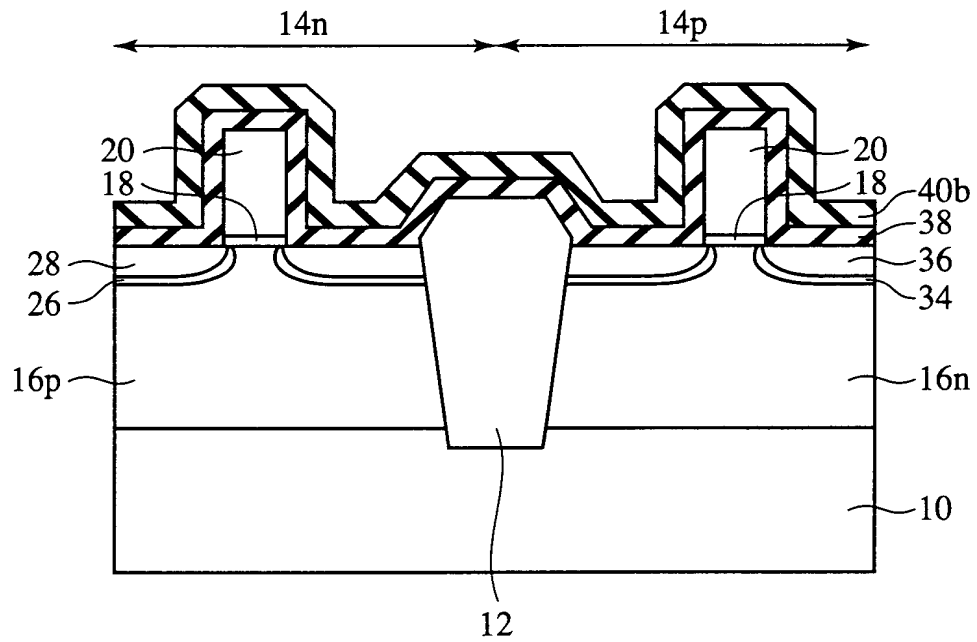
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FIG. 28



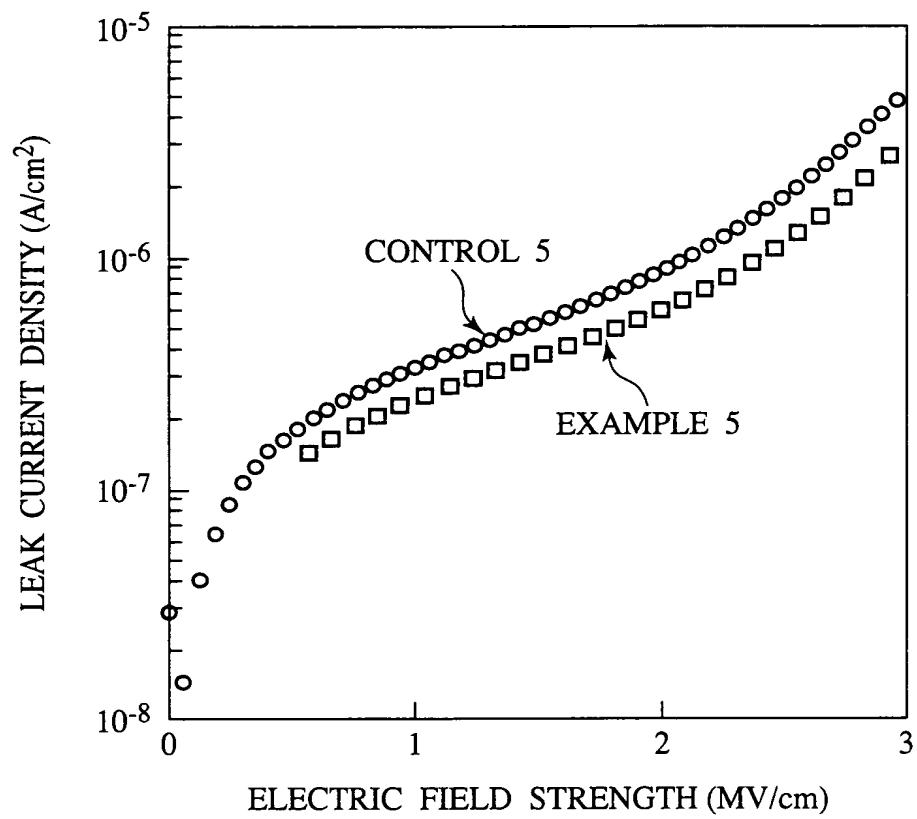
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FIG. 29



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FIG. 30



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FIG. 31A

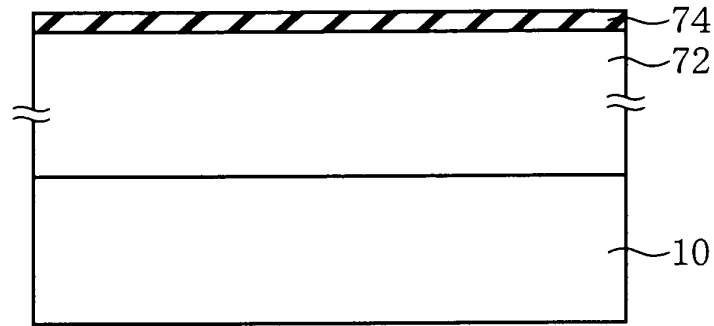


FIG. 31B

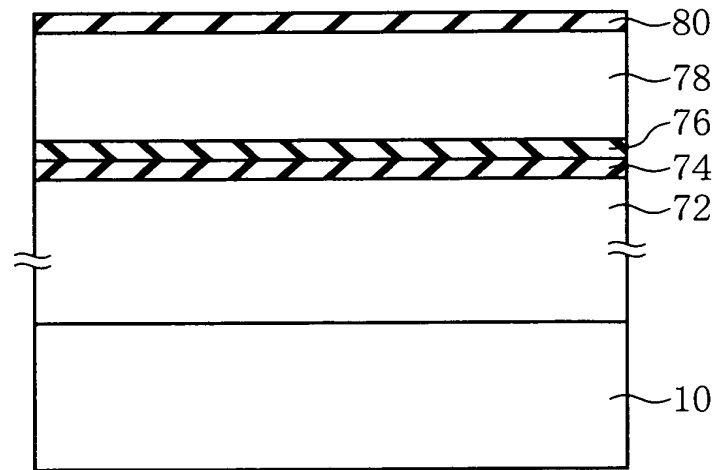


FIG. 32A

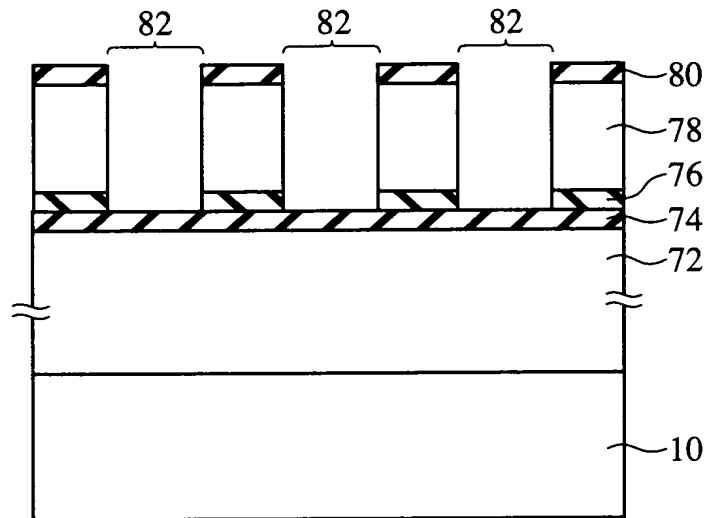
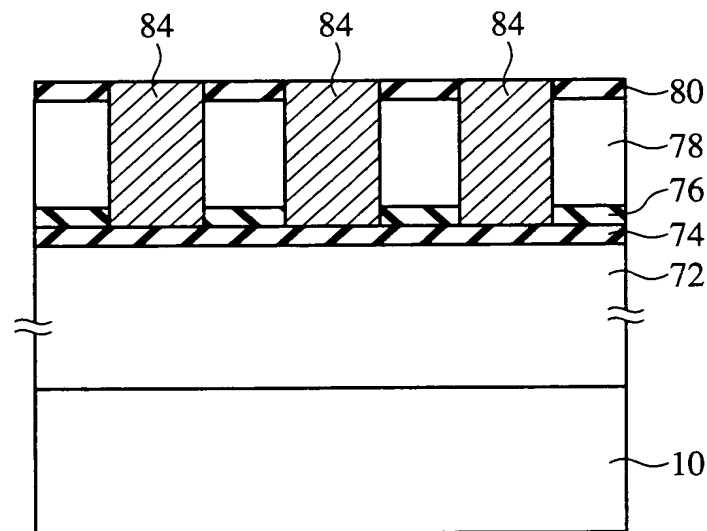


FIG. 32B



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FIG. 33

